Amendments to the Claims:

1. (Currently Amended) A routing method for a multiplex system configured to receive N input multiplexes and provide N' output multiplexes, wherein each output multiplex has M+L channels with $L \geq 0$, the method comprising:

receiving ~~having N inputs each designed to receive one of~~ N input multiplexes, wherein each input multiplex includes ~~having~~ M input channels and each input channel is configured ~~, each of the channels serving~~ to transport an input data packet and an associated input header, and wherein each input header identifies the corresponding data packet and specifies ~~, and N' outputs each serving to generate one of the N' output multiplexes each made up of output channels each serving to transport an output packet, each of the M channels used in the input multiplex comprising a data packet associated with an input header serving firstly to identify the packet and secondly to specify~~ at least one output to which it is to be routed;~~, wherein each of the N' output multiplexes has M+L channels with $L \geq 0$, and wherein the method implements the following operations:~~

~~for the data,~~ multiplexing ~~N input channels each having~~ the M multiplexed input data packets so as to generate an aggregate multiplexed signal comprising all of the multiplexed input data packets representing the N input multiplexes; ~~and~~

~~for each of the N' outputs, providing~~ distributing the aggregate multiplex signal among ~~the~~ inputs of N' processing chains, wherein each processing chain comprises M+L selection chains disposed in parallel ~~with access to the aggregate multiplexed signal~~;

~~for the input headers, performing demodulation~~ demodulating and decoding each of the M input headers;

~~for output headers, performing~~ encoding and ~~modulation~~ modulating ~~on the basis of~~ the demodulated and decoded input headers to generate output headers; ~~and~~

~~for the headers and the data,~~ selecting data packets from n of the M+L selection chains corresponding to the k^{th} output on the basis of the input headers corresponding to n packets that are to be routed to the k^{th} of the N' outputs, ~~the corresponding n data packets in the aggregate multiplexed signal~~ wherein said selecting step comprises selecting from the aggregate multiplexed signal each input multiplex that includes the data packets to be routed to the k^{th} output and selecting from each of said selected input multiplexes each data packet to be routed to the k^{th} output; and

~~and~~ multiplexing ~~these~~ the selected n data packets with ~~the~~ corresponding n output headers in order to generate the k^{th} output multiplex, where k = 1, 2, ..., N' ~~, wherein said selecting step comprises preliminary selection to select from the aggregate multiplexed signal the input multiplex(es) containing the data packets to be routed to the k^{th} output; and packet selection to select from each of said input multiplexes the data packet(s) to be routed to the k^{th} output~~.

2. (Original) A method according to claim 1, wherein L is a non-zero integer.

3. (Original) A method according to claim 1, wherein each of the input multiplexes comprises data packets and a signalling channel containing headers,

and wherein each of the N input multiplexes is demultiplexed in order to separate the headers from the data packets.

4. (Cancelled)

5. (Currently Amended) A method according to claim 1, wherein the aggregate multiplexed signal is generated by code- or frequency- or wavelength-division multiplexing for the N input multiplexes, a corresponding ~~said~~ code, frequency, or wavelength being allocated to each input multiplex, and wherein ~~said preliminary selection implements~~ the step of selecting from the aggregate multiplexed signal each input multiplex comprises demultiplexing using said code or frequency or wavelength.

6. (Currently Amended) A method according to claim 1 [[4]], wherein the aggregate multiplexed signal is generated by code- or frequency- or wavelength-division multiplexing for the N input multiplexes, a ~~said~~ corresponding code, frequency, or wavelength being allocated to each input multiplex, and wherein the step of selecting from the aggregate multiplexed signal each input multiplex comprises performing a ~~said selection implements~~ first code- or frequency- or wavelength-division demultiplexing operation ~~to perform the preliminary selection,~~ and wherein the step of selecting from each of said selected input multiplexes each data packet to be routed comprises performing a second code- or frequency- or wavelength-demultiplexing operation ~~to perform said packet selection~~.

7. (Currently Amended) A method according to claim 1, wherein said multiplexing of the selected data packets comprises ~~implements~~:

code- or frequency- or wavelength-division multiplexing of the data packets to be routed to the k^{th} output; and

code- or frequency- or wavelength-division multiplexing of the headers corresponding to said data packets to be routed to the k^{th} output.

8. (Original) A method according to claim 7, wherein the selected data packets and headers are multiplexed in the output multiplex of the k^{th} output.

9. (Currently Amended) A device for routing a plurality of input multiplexes, wherein each input multiplex includes at most M multiplexed data packets and a corresponding routing header ~~implementing the method according to claim 1~~, the device ~~presenting~~ comprising:

a first system comprising:

an [[N]] input multiplexing module ~~for~~ configured to ~~receiving~~ receive and ~~multiplexing~~ multiplex ~~the~~ N input multiplexes~~, each of which comprises up to M multiplexed packets,~~ and ~~for~~ configured to generate ~~generating~~ as output ~~said~~ an aggregate multiplexed signal;

a distributor circuit in communication with the input multiplexing module and configured to distribute ~~for distributing~~ said aggregate multiplexed signal to an input of each of N' processing chains, wherein each of the N' processing chains is allocated to one of ~~the~~ N' outputs of the device; and

~~each presenting~~ M+L selection circuits in parallel and each in communication with the distributor circuit~~; and~~, wherein each selection circuit ~~presenting~~ comprises in series a beam selector, a channel selector, and a channel converter; ~~and~~

a second system comprising:

a demodulation and decoding circuit ~~presenting N inputs for receiving~~ configured to receive the headers ~~corresponding to the data packets in~~ of each of the N input multiplexes and ~~for~~ configured to demodulate and decode ~~demodulating and decoding~~ said headers;

a processor circuit in communication with the demodulation and decoding circuit and with the beam selectors and channel selectors of the first system, wherein the processor circuit is configured to process ~~for processing~~ the demodulated and decoded input headers and to configure the beam selector and the channel selector in at least some of the M+L selection

circuits in each of the processing chains so that each said selection circuit selects a data packet for routing to ~~the~~ an output with which ~~it~~ the data packet is associated[[:]];

a header generator module in communication with the processor circuit and configured to generate ~~for generating~~, for each output of the device, ~~the~~ output headers corresponding to each ~~of the n~~ output data ~~packets~~ packet outputted from said selection circuits of the first system ~~of said output~~; and

an encoder and modulator circuit configured to encode and modulate ~~for encoding and modulating~~ the output headers generated by the header generator module;

~~and~~ wherein, for each of said selection circuits of the first system, the channel converter comprises ~~presents~~ means for adding to each of said data packets an output channel identification signal so as to place each ~~said n~~ output data packet ~~packets~~ belonging to the same output on different channels; and

~~and~~ wherein the device further includes an output multiplexer module for each of said N' outputs of the device configured to multiplex the data packets allocated to said outputs with the output headers corresponding to said data packets.